

(Invited) Semiconductor Nanostructures for Antireflection Coatings, Transparent Contacts, Junctionless Thermoelectrics and Li-Ion Batteries

To cite this article: Colm Glynn *et al* 2013 *Meet. Abstr.* **MA2013-01** 905

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Semiconductor Nanostructures for Antireflection Coatings, Transparent Contacts, Junctionless Thermoelectrics and Li-ion Batteries

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A nanostructured three-dimensional electrode using transparent conducting oxide (TCO) is an effective approach for increasing the efficiency of optoelectronic devices used in daily life. We summarise MBE grown tin doped indium oxide (ITO) with high conductivity and high work function for application as solar cell back contacts with excellent antireflection and plasmon polariton-assisted transmissivity¹. The growth of highly porous, large area ITO nanowire layers with superior optical and electrical properties is correlated to the graded porosity nanostructured layers.

For energy storage considerations such a Li-ion batteries, ITO has a theoretical reversible charge capacity of 883 mAh g⁻¹, making it ideal transparent and conductive Li-ion battery anode. We present detailed structural and electrochemical investigations of unprecedented defect-free indium-tin oxide and tin oxide nanostructures grown by molecular beam epitaxy completely without any heterogeneous seed of growth-facilitating phase. By controlling the growth conditions, a variety of high quality nanostructures are possible and their electrochemical response is correlated with specific structural changes during insertion, alloying and removal of lithium. We detail the correlation between electrochemical lithium insertion and removal processes during charging and discharging for these transparent battery anodes.

By using silicon, comparable optical, electrical and charge storage paradigms can also be examined and developed. We show here, that by controlling the topology and surface crystallinity of metal-assisted chemically etched Si nanowire layers and electrochemically formed 3D hetero-photonic and phononic crystal membranes, thermoelectric materials are realised by exploiting the variable thermal conductivity of silicon due to thermalized phonon processes², determined using *in-situ* temperature-dependent Raman scattering spectroscopy. This approach allows a range of processable thermoelectric silicon-based materials from quantum-confined nanostructure and topology variation, without necessitating a p-n junction.

Finally, as Si is receiving significant renewed interest for Li-ion batteries and emerging alternatives, we show recent work on the electrochemical response of Si and electrolessly etched Si nanowire layers to lithium insertion and removal³⁻⁶. Silicon has the highest theoretical specific charge capacity which has been realized with nanostructured silicon with limited cycle life. The specific details on nanostructured⁷ versus bulk Si to charging will also be summarized and correlated to cyclic voltammetric analysis of lithium insertion processes in n- and p-doped silicon. Overall, both Si and ITO exhibit a range of potentially exploitable properties for energy storage and generation.

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